

**FEE TRANSMITTAL SHEET**  
(FOR FY 2001)

**JAN 16 2002**  
PATENT & TRADEMARK OFFICE

**METHOD OF PAYMENT (Check One)**

1. ☒ The Commissioner is hereby authorized to charge any additional fee required under 37 C.F.R. §§ 1.16 and 1.17 and 1.136(a)(3) and credit any over payments to Deposit Account No.: 50-1266; Deposit Account Name: DORSEY & WHITNEY LLP

2. ☒ Check Enclosed

**FEE CALCULATION**

**1. BASIC FILING FEE**

Large Entity		Small Entity		Fee Description
Fee Code	Fee (\$)	Fee Code	Fee (\$)	
101	740	201	370	<input type="checkbox"/> Utility Filing Fee
106	330	206	165	<input type="checkbox"/> Design Filing Fee
108	740	208	370	<input type="checkbox"/> Reissue Filing Fee
114	160	214	80	<input type="checkbox"/> Provisional Filing Fee
<b>Subtotal (1)</b>				<b>\$0</b>

**2. EXTRA CLAIM FEES**

Current Claims	Prior	Extra	Fee	Fee Paid
Total	43	- 20	= 23 x \$ 18	= \$ 414
Ind.	8	- 3	= 5 x \$ 84	= \$ 420
Multiple Dependent Claims				\$
<b>Subtotal (2)</b>				<b>\$834</b>

Large Entity		Small Entity		Fee Description
Fee Code	Fee (\$)	Fee Code	Fee (\$)	
103	18	203	9	Claims in excess of 20
102	84	202	42	Independent claims in excess of 3
104	280	204	140	Multiple dependent Claim
109	80	209	40	Reissue independent claims over original patent
110	18	210	9	Reissue claims in excess of 20 and over original patent

**Complete if Known**

Application No.	09/964,113
Filing Date	September 25, 2001
First Named Inventor	Brent Keeth
Group Art Unit	2133
Examiner	Not Yet Assigned
Atty. Docket Number	500426.02

**RECEIVED**  
**FEB 05 2002**  
**Technology Center 2100**

**FEE CALCULATION (Continued)**

**3. ADDITIONAL FEES**

Large Entity		Small Entity		Fee Description	Fee paid
Fee Code	Fee (\$)	Fee Code	Fee (\$)		
105	130	205	65	Surcharge - Late filing fee or oath	\$
127	50	227	25	Surcharge - late provisional filing fee or cover sheet	\$
139	130	139	130	Non-English specification	\$
147	2,520	147	2,520	For Filing a Request for Reexamination	\$
195	300	196	300	Publication (early or Republication)	\$
115	110	215	55	Extension for reply within first month	\$
116	400	216	200	Extension for reply within 2 <sup>nd</sup> month	\$
117	920	217	460	Extension for reply within 3 <sup>rd</sup> month	\$
118	1,440	218	720	Extension for reply within 4 <sup>th</sup> month	\$
128	1,960	280	980	Extension for reply within 5 <sup>th</sup> month	\$
120	320	220	160	Filing a brief in support of an appeal	\$
121	280	270	140	Request for oral hearing	\$
148	110	248	55	Terminal Disclaimer Fee	\$
140	110	240	55	Petition to revive - unavoidable	\$
141	1,280	241	640	Petition to revive - unintentional	\$
142	1,280	242	640	Utility/Reissue issue fee (+ advance copies)	\$
143	460	243	230	Design issue fee (+ advance copies)	\$
122	130	122	130	Petitions to the Commissioner	\$
123	50	123	50	Petitions related to provisional applications	\$
126	180	126	180	Submission of IDS	\$
581	40	81	40	Recording each patent assignment per property (times number of properties)	\$
179	740	279	370	Request for Continued Examination (RCE)	\$
Other fee (specify)					\$
<b>Subtotal (3)</b>				<b>\$0</b>	
<b>Total Amount of Payment:</b>				<b>\$834</b>	

Submitted by:

Name: Kimton N. Eng	Reg. No.: 43,605	Telephone: (206) 903-8800
Signature: <u>Kimton N. Eng</u>		Date: <u>November 14, 2001</u>



#58/91502  
**RECEIVED**  
FEB 05 2002  
Technology Center 2100  
PATENT

I hereby certify that on the date specified below, this correspondence is being deposited with the United States Postal Service as first-class mail in an envelope addressed to the Commissioner of Patents, Washington, DC 20231.

November 16, 2001

Date

Ayesha S. Wilks

Ayesha S. Wilks

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant : Brent Keeth

Attorney Docket No.: 500426.02

Serial No. : 09/964,113

Group Art Unit : 2133

Filed : September 25, 2001

Examiner : Not Yet Assigned

Title : METHOD AND APPARATUS FOR DATA COMPRESSION IN MEMORY DEVICES

**SUPPLEMENTAL PRELIMINARY AMENDMENT**

Commissioner of Patents  
Washington, D.C. 20231

Sir:

In the Claims:

Please cancel claim 1.

Please add new claims 56-98 as follows:

--56. (New) A memory device having a normal mode and a test mode, the memory device comprising:

first and second memory arrays, each memory array having memory cells arranged in rows and columns, each column of memory cells having a pair of complementary digit lines;

01/25/2002 FRANKLIN DODD/TS 1158-111

01 50:102  
12 50:102

500.00 00  
500.00 00